FIG. 1

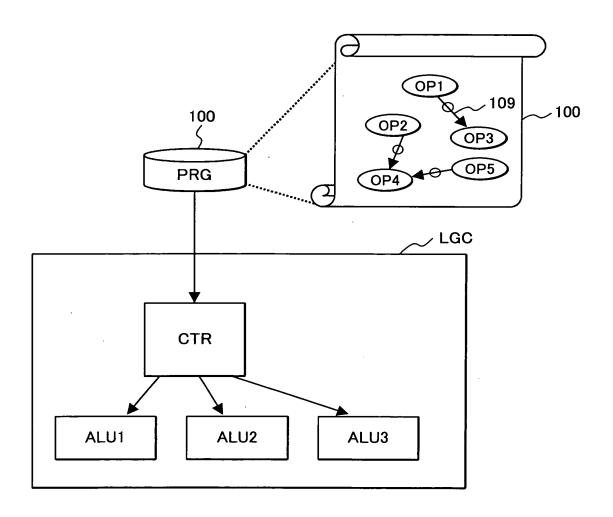


FIG. 2

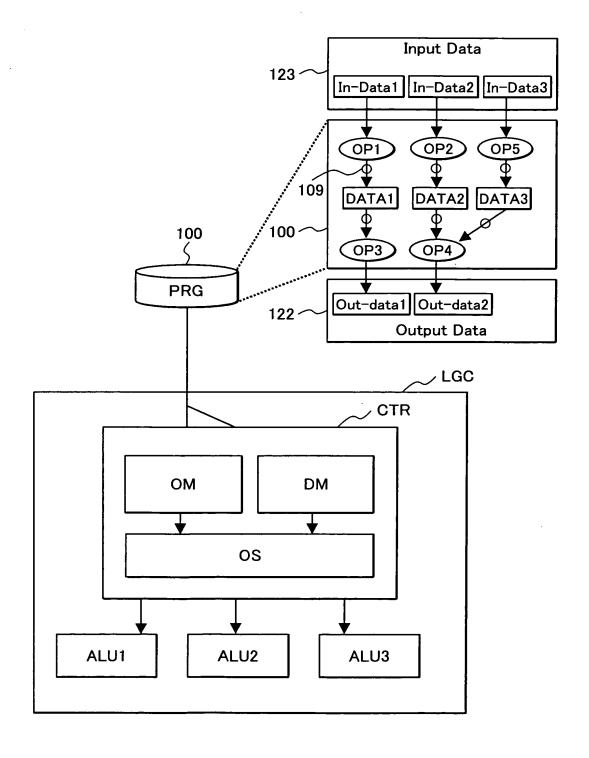


FIG. 3

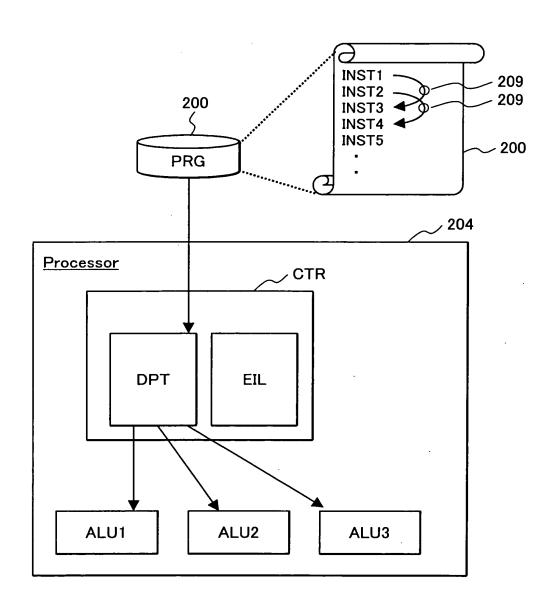


FIG. 4

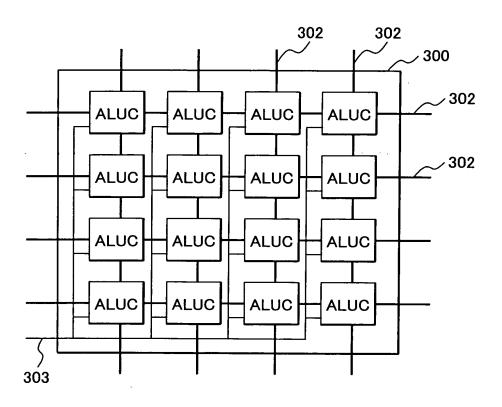


FIG. 5

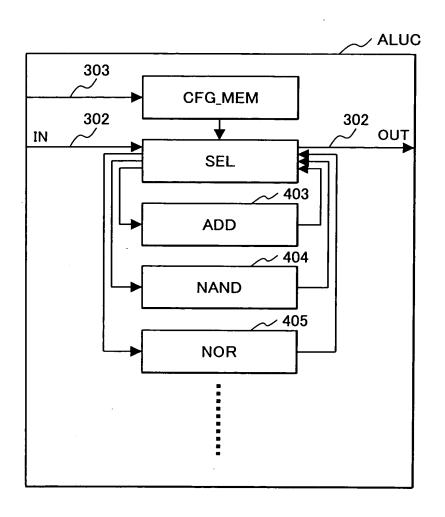


FIG. 6

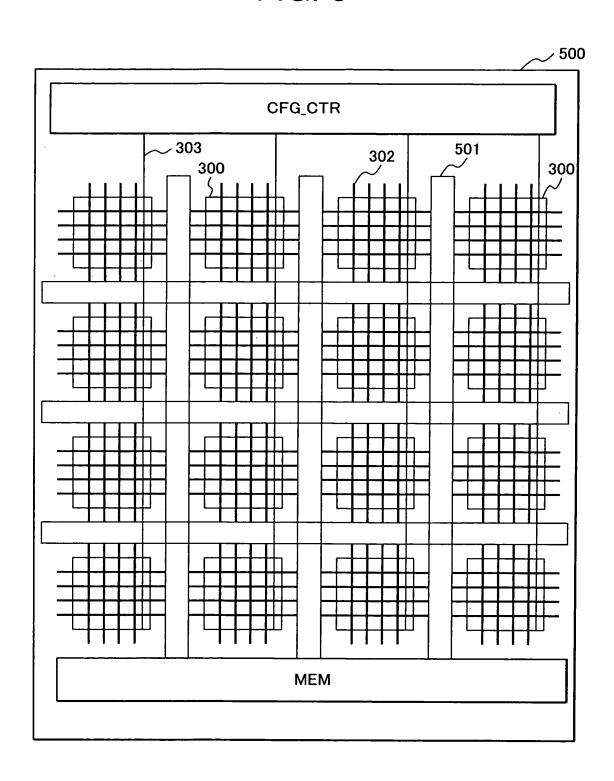


FIG. 7

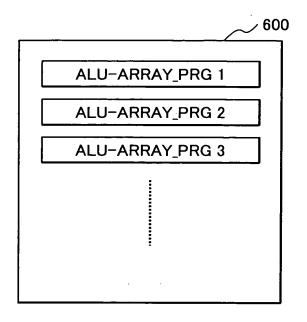


FIG. 8

ALU-ARRA	Y_PRG1
In-data	
Out-data	
ALUC_ PRG 1-1	
ALUC_ PRG 1-2	

FIG. 9

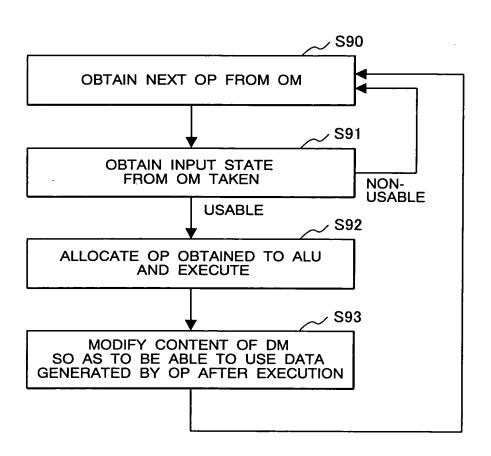


FIG. 10

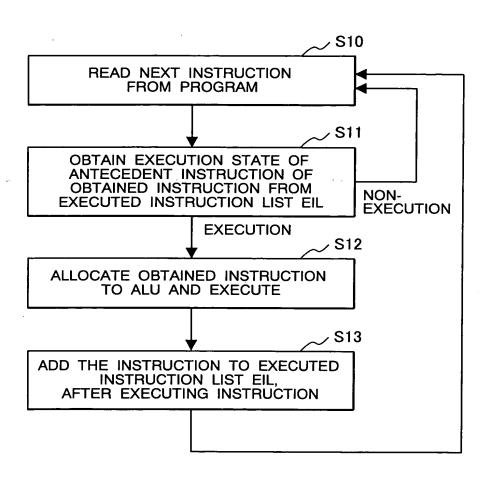


FIG. 11

	ON
OP1	In-Data1
OP2	In-Data2
OP3	DATA1
OP4	DATA2,3
OP5	In-Data3

FIG. 12

	DM
OP1	In-Data1
OP2	In-Data2
OP3	
OP4	•
OP5	In-Data3